

20[™] INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING



CALL FOR PAPERS

The 20th annual IEEE International Conference on High Performance Computing (HiPC 2013) will be held at the Hyderabad International Convention Center in Hyderabad, India, from December 18-21, 2013. It will serve as a forum for researchers from around the world to present their current research efforts and findings, and will serve as a venue for stimulating discussions and highlighting high performance computing (HPC) related activities in Asia. The conference has a history of attracting participation from reputed researchers from all over the world.

HiPC 2013 will focus on the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions consisting of contributed papers, the conference will include invited presentations, a student research symposium, tutorials, and vendor presentations in the industry, user and research symposium. Additional information about the HiPC 2013 conference and HiPC series of meetings is available at http://www.hipc.org.

Authors are invited to submit original unpublished research manuscripts that demonstrate current research in all areas of high performance computing including design and analysis of parallel and distributed systems, embedded systems, and their applications in scientific, engineering, and commercial areas.

Topics of interest include but are not limited to:

> High-Performance Computing

- > Parallel and Distributed Algorithms / Applications
- > Parallel and Distributed Architectures / Software
- > Parallel Languages and Programming Environments
- > Hybrid Parallel Programming with GPUs and Accelerators
- > Load Balancing, Scheduling and Resource Management
- > Resilient/Fault-Tolerant Algorithms and Systems
- > Scientific/Engineering/Commercial Applications and Workloads
- > Emerging Applications such as Biotechnology and Nanotechnology
- > Cluster, Cloud, and Grid Computing
- > Heterogeneous Computing
- > Interconnection Networks and Architectures
- > Scalable Servers and Systems
- > High Performance/Scalable Storage Systems
- > Power-Efficient and Reconfigurable Architectures
- > Compiler Technologies for High-Performance Computing
- > Software Support and Advanced Micro-architecture Techniques
- > Operating Systems for Scalable High-Performance Computing

One or more best paper awards will be given for outstanding contributed papers. Further details about call for student research symposium, workshops, tutorials, and exhibits, as well as submission guidelines are available at the conference website http://www.hipc.org.

GENERAL CO-CHAIRS BADRINATH RAMAMURTHY, HP, India RAMA GOVINDARAJU, Google, California, USA

VICE-GENERAL CHAIR JIGAR HALANI, WIPRO, India

PROGRAM CHAIR

YVES ROBERT, ENS Lyon, France
PROGRAM VICE-CHAIRS

ALGORITHMS: DENIS TRYSTRAM Inria Grenoble, France APPLICATIONS: DR. XIAOYE S. LI Lawrence Berkeley Nat. Lab., USA ARCHITECTURE: MITSUHISA SATO University of Tsukuba, Japan SOFTWARE: THOMAS HÉRAULT University of Tennessee Knoxville, USA

STEERING COMMITTEE CHAIR VIKTOR K. PRASANNA, University of Southern California, USA

IMPORTANT DATES

Apr 02, 2013	Conference Paper Submission Begins
May 16, 2013	> Conference Paper Submission Ends
Mar 01, 2013	> Workshop Proposals Due
July 31, 2013	> Notification of Acceptance/Rejection
Aug 18, 2013	> Camera-Ready Paper Submission
Sep 16, 2013	> Student Symposium Submissions Due
Oct 21, 2013	> Student Symposium Accept/Reject Decisions
Nov 14, 2013	> Early Registration Deadline

CO-SPONSORED BY

IEEE Computer Society Technical Committee on Parallel Processing (TCPP) • HiPC Education Trust



